

EMI Suppression Capacitor, Ceramic Disc, Class X2, 400 V_{AC}



FEATURES

- Complying with IEC 60384-14
- High reliability
- Radial leads
- Singlelayer AC disc safety capacitors
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
COMPLIANT

APPLICATIONS

- X2 according to IEC 60384-14
- Line-to-line filtering (Class X)
- EMI / RFI suppression and filtering

DESIGN

The capacitors consist of a ceramic disc of which both sides are silver-plated. Connection leads are made of tinned copper having a diameter of 0.025" (0.64 mm). The capacitors may be supplied with radial kinked or straight leads having a lead spacing of 0.375" (9.5 mm) or 0.250" (6.4 mm). The standard tolerance is $\pm 20\%$. Coating is made of flame retardant epoxy resin in accordance with "UL 94 V-0."

QUICK REFERENCE DATA		
DESCRIPTION	VALUE	
Ceramic Class	2	
Ceramic Dielectric	Y5V	Z5U
Voltage (V _{AC})	400	400
Min. Capacitance (pF)	9000	10 000
Max. Capacitance (pF)	100 000	10 000
Mounting	Radial	

INSULATION RESISTANCE

Min. 1000 Ω F

TOLERANCE ON CAPACITANCE

 $\pm 20\%$

DISSIPATION FACTOR

2.0 % max. at 1 kHz; 1 V

CERAMIC DIELECTRIC

Y5V, Z5U

CATEGORY TEMPERATURE RANGE

-25 °C to +125 °C

CLIMATIC CATEGORY ACC. TO EN 60068-1

25 / 125 / 21

OPERATING TEMPERATURE RANGE

-30 °C to +125 °C ⁽¹⁾

Note

⁽¹⁾ For explanation about the difference of operating temperature range and temperature characteristic of capacitance, please see www.vishay.com/doc?48299

CAPACITANCE RANGE

9 nF to 0.1 μ F

RATED VOLTAGE

IEC 60384-14:

X2: 400 V_{AC}, 50 Hz

DIELECTRIC STRENGTH BETWEEN LEADS

Component test, 100 % test at production line:

1250 V_{AC}, 50 Hz, 2 s

As repeated test at customer side admissible only once with:

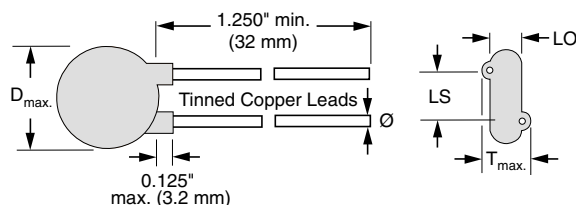
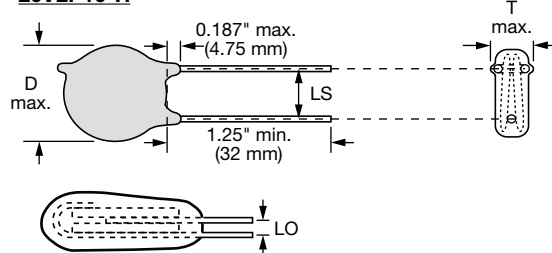
1080 V_{AC}, 50 Hz, 2 s

Random sampling test (destructive test):

1250 V_{AC}, 50 Hz, 60 s

DIELECTRIC STRENGTH OF BODY INSULATION

2300 V_{AC}, 50 Hz, 60 s (destructive test)

DIMENSIONS in inches (millimeters)

20VLP10-R

ORDERING INFORMATION, CERAMIC X2 CAPACITORS 20VL

C (μ F)	TOL. (%)	D _{max.} DIAMETER INCH (mm)	T _{max.} THICKNESS INCH (mm)	WIRE SIZE		LS LEAD SPACE INCH (mm) ± 1 mm	LO LEAD OFFSET INCH (mm) ± 0.5 mm	ORDERING CODE
Y5V								
0.009	± 20	0.530 (13.5)	0.150 (3.8)	22	0.025 (0.64)	0.375 (9.5)	0.055 (1.4)	20VLD90-R
0.010	± 20	0.620 (15.7)	0.150 (3.8)				0.063 (1.6)	20VLS10-R
0.020	± 20	0.720 (18.3)	0.150 (3.8)				0.055 (1.4)	20VLS20-R
0.100	± 20	0.950 (24.1)	0.230 (5.8)				0.067 (1.7)	20VLP10-R
Z5U								
0.010	± 20	0.530 (13.5)	0.160 (4.1)	22	0.025 (0.64)	0.250 (6.4)	0.067 (1.7)	20VLSS10-R

Notes

- Alternate lead spacings of 7.5 mm and 10 mm are available bulk or tape and reel on request
- Minimum lead clearance according to IEC 60384-14: 0.118" (3 mm)

TAPE AND REEL OPTIONS

Part number codes and specifications for tape and reel packaging are found in the general information document www.vishay.com/doc?23140.

APPROVALS

IEC 60384-14 - Safety tests

This approval together with CB test certificate substitutes all national approvals.

CB Certificate (www.vishay.com/doc?22247)

X2-capacitor: CB test certificate:

DE1-63496

9 nF to 0.1 μ F

400 V_{AC}



VDE (www.vishay.com/doc?22246)

X2-capacitor: VDE marks approval:

40003982

9 nF to 0.1 μ F

400 V_{AC}

DIN EN 60384-14 VDE 0565-1-1 - Safety tests



Underwriters Laboratories Inc. (www.vishay.com/doc?22245)

X2-capacitor: UL test certificate:

E99264

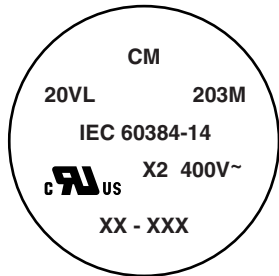
9 nF to 0.1 μ F

400 V_{AC}

UL 60384-14, CSA E60384-1, CSA E60384-14

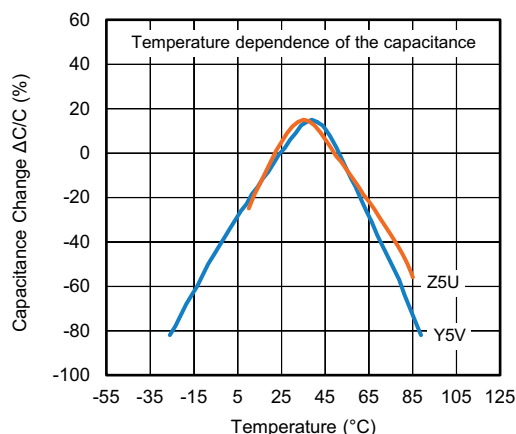


MARKING

Sample 	  PN: 20VLP10-R Cap.: 100nF ± 20% Ur.: X2(400~) Qty.: 125 IEC 60384-14:2013: C_{UL} US PO: 0034761587/0001 LOT1: 34761587 LOT2: BATCH NO.: 201949CZ R.C.: 7032 DC1: 1949 DC2: S.L.: 0010 SN: 292126873015  
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Notes

- Marking IEC 60384-14 does not apply for $\varnothing \leq 9$ mm
- “XX - XXX” is a placeholder for date code and lot number:
“XX -” is the year and month according to IEC 60062
“- XXX” is the last 3 digits of the lot number

CAPACITANCE CHANGE VS. TEMPERATURE (Typical)

STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +40 °C, relative humidity up to 60 % RH). Class 2 ceramic dielectric capacitors are also subject to aging see general information (www.vishay.com/doc?23140).

SOLDERING
SOLDERING SPECIFICATIONS

Soldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)

	SOLDERABILITY	RESISTANCE TO SOLDERING HEAT
Soldering temperature	(235 ± 5) °C	(260 ± 5) °C
Soldering duration	(2 ± 0.5) s	(10 ± 1) s
Distance from component body	≥ 2 mm	≥ 5 mm

SOLDERING RECOMMENDATIONS

Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see table above) should not be exceeded. Exposing the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.



When soldering radial leaded ceramic capacitors with a soldering iron, it should be performed under the following conditions and should not exceed:

- Maximum temperature of iron-tip: 400 °C
- Maximum soldering iron wattage: 50 W
- Maximum soldering time: 3.5 s

Failure to follow the above cautions may result, in worst case, in short circuit or cause fuming or thermo-mechanical damage when the product is used.

Leaded ceramic capacitors are not designed for reflow process or dipping the body into a solder melt.

CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions:

- Maximum rinse bath capacity output: 20 W/liter
- Maximum rinsing time: 300 s
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to mechanical damage

SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method:

IEC 60068-2-45 (method XA)

MOUNTING

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. In order to avoid such failures we are offering different lead wire designs (e.g. straight, inline, inside crimp, outside crimp etc.) If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating. If a defined product stop is required for mounting on a PCB, a mechanically formed product stop or a mounting tool should be used.

OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS	
General Information	www.vishay.com/doc?23140
CB Test Certificate	www.vishay.com/doc?22247
VDE Marks Approval	www.vishay.com/doc?22246
UL Test Certificate	www.vishay.com/doc?22245



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